

This question paper contains 4 printed pages.]

1800

Your Roll No.

B.Sc. (Hons.) Computer Sc. / V Sem. A
Paper – 503 : MICROPROCESSORS
(Admissions of 2001 and onwards)

Time : 3 Hours

Maximum Marks : 75

*(Write your Roll No. on the top immediately
on receipt of this question paper.)*

Attempt all questions.

Parts of questions should be attempted together.

1. (a) Differentiate between Trap (T) and Interrupt flag
(I) of EFLAG register. 2
- (b) Find the memory address of the next instruction
executed by the microprocessor, when operated in
the real mode, for the following CS : IP
combinations : 2
- (i) CS = 2300 H and IP = 1A00H
- (ii) CS = 3456 H and IP = AB CDH
- (c) What value is placed in the page table to redirect
linear address 20000000 H - 30000000 H ? 2
- (d) What are microprocessor control registers in
memory paging ? 2

[P.T.O.]

Microprocessor And Its Applications Question Paper

RD Boyd



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