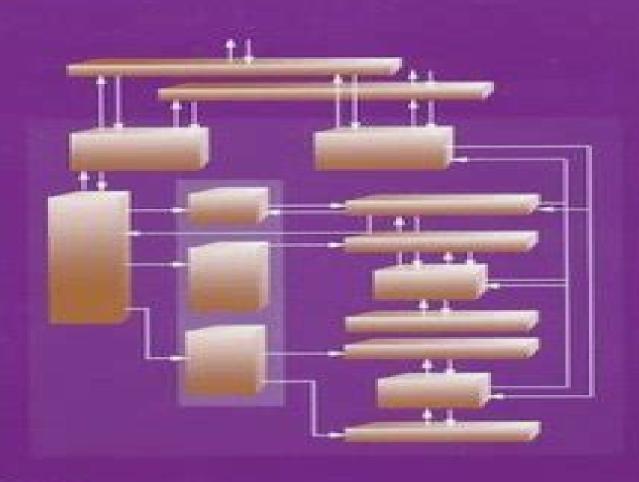
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On-Line Testing for VLSI Michael Nicolaidis, Yervant Zorian, Dhiraj Pradhan, 2013-03-09 Test functions fault detection diagnosis error correction repair etc that are applied concurrently while the system continues its intended function are defined as on line testing In its expanded scope on line testing includes the design of concurrent error checking subsystems that can be themselves self checking fail safe systems that continue to function correctly even after an error occurs reliability monitoring and self test and fault tolerant designs On Line Testing for VLSI contains a selected set of articles that discuss many of the modern aspects of on line testing as faced today The contributions are largely derived from recent IEEE International On Line Testing Workshops Guest editors Michael Nicolaidis Yervant Zorian and Dhiraj Pradhan organized the articles into six chapters In the first chapter the editors introduce a large number of approaches with an expanded bibliography in which some references date back to the sixties On Line Testing for VLSI is an edited volume of original research comprising invited contributions by leading researchers Parallel Image Analysis Akira Nakamura, Maurice Nivat, Ahmed Saoudi, Patrick S.P. Wang, Katsushi Inoue, 1992-12-02 This volume contains the papers selected for presentation at the Second International Conference on Parallel Image Analysis ICPIA 92 held in Ube Japan December 21 23 1992 The conference topics are data structures parallel algorithms and architectures neural networks computational vision syntactic generation and recognition and multidimensional models The first meeting with these topics was the International Colloquium on Parallel Image Processing which took place in Paris in June 1991 The aim of the meetings is to bring together specialists from various countries who are interested in the topics and to stimulate theoretical and practical research in the field of parallel image processing and analysis The volume contains three invited papers a summary of a tutorial lecture and twenty selected and refereed communications **Highly Parallel Computing** George S. Almasi, Allan Gottlieb, 1994 This second edition includes new exercises for each chapter a quantitative treatment of speedup seismic migration using a workstation network as a parallel computer recent changes in technology more languages fat trees wormhole switching new SIMD hardware an expanded section on CM 2 new MIMD hardware using workstation clusters as a MIMD system and directory based caches Annotation copyright by Book News Inc Portland OR Computer Architecture James M. Feldman, Charles T. Retter, 1994 IEEE Computer Society Workshop on Computer Architecture for Pattern Analysis and Image Database Management, 1985 Computer Architecture and Organization John P. Hayes, 1988 1985 IEEE Computer Society Workshop on Computer Architecture for Pattern Analysis and Image Database Management, Miami Beach,

Florida, November 18-20, 1985, 1985 Computer Architecture for Pattern Analysis and Image Database Management, 1981 ICSP2002 Yuan Baozong, 2002 6th International Conference on Signal Processing Baozong Yuan, Xiaofang Tang, 2002 McGraw-Hill encyclopedia of science & technology McGraw-Hill, 2002

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