

**3**Answer any **four** parts :**5×4=20**

- (a) Sketch the memory read Bus Cycle timings for 8086. Will there be any difference in case of 8088?
- (b) From pin description, it seems that 8086 has only 16 AD lines i.e. AD0-AD15. The physical address space however, is larger than  $2^{16}$ . How this contradiction is handled? Explain with a neat diagram.
- (c) Can 8087 be used as an independent processor? Compared to 8086, does 8087 have a separate (i) address space, (ii) instruction set? Explain.
- (d) Write a short note on 8284A. Is the RESET in 8086 synchronous or asynchronous?
- (e) Explain the floating point number formats used in a PC with 8087. What are the advantages and disadvantages of using floating point formats?
- (f) Write a short note on 8086 status pins in Maximum Mode.

**4**Answer any **two** parts :**10×2=20**

- (a) What is interrupt driven data transfer? Is it a form of programmed data transfer or not? While writing an interrupt service subroutine, what special features are usually incorporated extra in comparison to a normal subroutine? Justify. What is the difference in execution of RET and IRET instructions?
- (b) Give the Bus Cycle timing diagrams for IN and OUT instructions. How are they different from comparable MOV instructions? Can MOV instruction be ever used for I/O operation? If yes, what the I/O type is then called? If no, why not?

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