

MOSFET Cross Reference Guide

Industry Part Number	Recommended Fairchild Device	Package
2N7000	2N7000	TO-92, N
2N7002	2547/00/2	SOT-23, N
BS170	BS170	TO-92, N
BS270	BS270	TO-92, N
855100	855100	TO-92, N
BSS110	BSS110	TO-92, P
BSS123	855123	SOT-23, N
BSS138	855138	SOT-23, N
BSS84	BSS84	SOT-23, P
FF1010E	NDP7061L	TO-220, N
RF1010N	NDP7060L	TO-220, N
FIF1104	NDP7052L	TO-220, N
RF1310N	NDP710A	TO-220, N
FF13105	NDB710A	TO-263, N
FIF2807	NDP708A	TO-220, N
FF2907S	NDB408A	TO-263, N
FF3205	NDP7052L	TO-220, N
FF3710	NDP710A	TO-220, N
RF510	NDP410A	TO-220, N
FF520	NDP410A	TO-220, N
FIFS20N	NDP510A	TO-220, N
RF520S	NDB410A	TO-263, N
FF530	NDP510A	TO-220, N
FIFSOON	NDP510A	TO-220, N
FIFS40	NDP616A	TO-220, N
FIFS40N	NDP710A	TO-220, N
FIF540S	NDP610A	TO-220, N
FF7101	ND/S9956A	SO-8, Dual N
RF7102	ND/S9959	SO-8, Dual N
FF7103	NDS9955	SO-s, Dual N
F8F7104	ND/99953A	SO-8, Dual P
FIF7105	NDS9952A	SO-8, Comp N/P
FIF7106	NDS9958	SO-8, Comp N P
FF7107	NDS9958	SO-8, Comp N/P
FIF7201	FDS6612A	SO-8, N
FIF7202	NDIS9400A	SO-8, P

Industry Part Number	Recommended Fairchild Device	Package
F8F7203	FDS9435A	50-a, P
FIF7204	NDS8434A	SO-8, P
RF7205	FDS9435A	50-8, P
IRF7206	FDS9435A	SO-a, P
IRF7301	FDS8926A	SO-8, Dual N
RF7303	FDSepacA	SO-8, Dual N
IRIF7304	FDS8934A	SO-8, Dual P.
RF7306	FDS8947A	SO-8, Dual P
FF7307	FDSes28A	SO-8, Comp N/P
IRF7309	FDS8958A	SO-8, Comp N/P
IRF7311	FDS8926A	SO-8, Dual N
RF7313	FD/56912A	SO-8, Dual N
FIF7314	FDS#904A	SO-8, Dual P
RF7316	FDS8947A	SO-a, Dual P
FIF7317	FDS8928A	SO-8, Comp N/P
IRIF7319	FD/S8958A	SO-8, Comp N/P
RF7389	FDSe958A	SO-8, Comp N/P
FIF7401	NDS8426A	SO-8, N
IRIF7403	FDIS6612A	SO-8, N
RF7404	NDS8434A	SO-8, P
F8F7406	FDIS9435A	SO-8, P
RF7413	FDISeegoA	SO-8, N
IRF7413A	FDS6690A	SO-8, N
IRF7416	NDS8405A	SO-8, P
F8F75011	NOHIBOOTN	Micrott, Dual N
FF75001	NOHescon	Micros, Dual N
RF75041	NDHsoopP	Micros, Dual P
RF7506*	NOH8504P	Micros, Dual P
IRIF7507°	NDH8320C	Microst, Comp N/P
F6F75091	NDH8520C	Micrott, Comp N/P
RF7601*	NOHISSTIN	Micros, N
PF7600*	NOH8436	Micros, N
RF7604*	NDHIstop	Microst, P
FIF7606	NOH8447	Micros, P
IRF7805	FDS6690A	SO-8, N
IRIF7007	FDIS6612A	SO-8, N

PARTIES.

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Analog Devices, inc

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MOSFET Modeling & BSIM3 User's Guide Yuhua Cheng, Chenming Hu, 2007-05-08 Circuit simulation is essential in integrated circuit design and the accuracy of circuit simulation depends on the accuracy of the transistor model BSIM3v3 BSIM for Berkeley Short channel IGFET Model has been selected as the first MOSFET model for standardization by the Compact Model Council a consortium of leading companies in semiconductor and design tools In the next few years many fabless and integrated semiconductor companies are expected to switch from dozens of other MOSFET models to BSIM3 This will require many device engineers and most circuit designers to learn the basics of BSIM3 MOSFET Modeling BSIM3 User s Guide explains the detailed physical effects that are important in modeling MOSFETs and presents the derivations of compact model expressions so that users can understand the physical meaning of the model equations and parameters It is the first book devoted to BSIM3 It treats the BSIM3 model in detail as used in digital analog and RF circuit design It covers the complete set of models i e I V model capacitance model noise model parasitics model substrate current model temperature effect model and non quasi static model MOSFET Modeling BSIM3 User's Guide not only addresses the device modeling issues but also provides a user's quide to the device or circuit design engineers who use the BSIM3 model in digital analog circuit design RF modeling statistical modeling and technology prediction This book is written for circuit designers and device engineers as well as device scientists worldwide It is also suitable as a reference for graduate courses and courses in circuit design or device modelling Furthermore it can be used as a textbook for industry courses devoted to BSIM3 MOSFET Modeling BSIM3 User's Guide is comprehensive and practical It is balanced between the background information and advanced discussion of BSIM3 It is helpful to experts and students alike Essential Circuits Reference Guide John Markus, Charles D. Weston, 1988 A guide to research this volume includes 925 studies of Chaucer written between 1900 and 1984 Each entry is listed once alphabetically under an appropriate topic heading or under the title of the work it treats most directly The annotations provide bibliographic information identify the primary focus of the item annotated and summarize its content See entry PR1868 These classic circuits were chosen from Markus Sourcebook of electronic circuits 1968 Electonics circuits manual 1971 and Guidebook of electronics circuits 1974 With circuit integration onto chips many older circuits have become obsolete This guide is a distillation of those circuits still in use today for which parts are still available Annotation copyrighted by Book News Inc Portland OR Discrete Semiconductor Products Databook ,1989

Electronics Engineer's Reference Book L. W. Turner, Leslie William Turner, 1976 MOSFET Models for SPICE Simulation William Liu, 2001-02-21 An expert guide to understanding and making optimum use of BSIM Used by more chip designers worldwide than any other comparable model the Berkeley Short Channel IGFET Model BSIM has over the past few years established itself as the defacto standard MOSFET SPICE model for circuit simulation and CMOS technology development Yet until now there have been no independent expert guides or tutorials to supplement the various BSIM

manuals currently available Written by a noted expert in the field this book fills that gap in the literature by providing a comprehensive guide to understanding and making optimal use of BSIM3 and BSIM4 Drawing upon his extensive experience designing with BSIM William Liu provides a brief history of the model discusses the various advantages of BSIM over other models and explores the reasons why BSIM3 has been adopted by the majority of circuit manufacturers He then provides engineers with the detailed practical information and guidance they need to master all of BSIM's features He Summarizes key BSIM3 components Represents the BSIM3 model with equivalent circuits for various operating conditions Provides a comprehensive glossary of modeling terminology Lists alphabetically BSIM3 parameters along with their meanings and relevant equations Explores BSIM3 s flaws and provides improvement suggestions Describes all of BSIM4 s improvements and new features Provides useful SPICE files which are available online at the Wiley ftp site **Student Reference Manual** for Electronic Instrumentation Laboratories Stanley Wolf, Richard F. M. Smith, 1990 This book shows students how to become proficient users of electronic measuring instruments and offers a practical understanding of electrical laboratory **1993 Applications Reference Manual** Analog Devices, inc,1993 practices TMOS Power MOSFET Motorola, Inc,1986 **IEEE Circuits & Devices** ,2002 EDN, Electrical Design News ,1996 Ouick Reference Guide Ferranti System Integration Kurt Hoffmann, 2004-04-16 The development of large scale integrated systems on a Electric Inc,1984 chip has had a dramatic effect on circuit design methodology Escalating requirements for low power high chip density circuits and systems have resulted in increasingly complex Bipolar CMOS and BICMOS technologies In order to design cost effective and reliable systems a thorough understanding of the relationship between the individual components and their impact on the performance of the integrated system is required Combining coverage of semiconductor physics digital VLSI design and analog integrated circuits in one volume this book will appeal to practising engineers circuit designers and physicists working in IC production product engineering quality management and quality testing Postgraduate students in microelectronics electrical and computer engineering will also find this book a useful reference Midwest **Electronic Design** ,1989 Device Modeling for Analog and RF CMOS Circuit Design Trond Ytterdal, Yuhua Cheng, Tor A. Fjeldly, 2003-05-07 Bridges the gap between device modelling and analog circuit design Includes dedicated software enabling actual circuit design Covers the three significant models BSIM3 Model 9 and EKV Presents practical guidance on device development and circuit implementation. The authors offer a combination of extensive academic and industrial experience

Paul Uyemura, 2006 This book teaches the principles of physical design layout and simulation of CMOS integrated circuits It is written around a very powerful CAD program called Microwind that is available on the accompanying CD ROM Featuring a friendly interface Microwind is both educational and useful for designing CMOS chips

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