LABORATORY MANUAL

ELECTRONIC DEVICES & CIRCUITS LAB II B. Tech I-SEM



DEPARTMENT OF

ELECTRONICS & COMMUNICATION ENGINEERING

Malla Reddy College Of Engineering and Technology

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Laboratory Manual Electronic Devices Circuits Lab

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Gordan, Costin Miron (inginer.)

Laboratory Manual Electronic Devices Circuits Lab:

Electronic Devices and Circuits Laboratory Manual Srinivasa Murthy, 2015-10-03 This is a Electronic Devices and Circuits laboratory Manual meant for II year Electronics Electrical engineering students All the circuits in this book ar tested Laboratory Manual for Electronic Devices and Circuits David A. Bell, 2001 This lab manual accompanies Electronic Devices and Circuits 4 e Fundamentals of Electronic Devices and Circuits Lab Manual David Bell, 2009-11-22 The laboratory investigations in this manual are designed to demonstrate the theoretical principles set out in the book Fundamentals of Electronic Devices and Circuits 5 e A total of 43 laboratory investigations are offered involving the construction and testing of the circuits discussed in the textbook Each investigation can normally be completed within a two hour period The procedures contain some references to the textbook however all necessary circuit and connection diagrams are provided in the manual so that investigations can also be preformed without the textbook Laboratory Manual For Electronic Devices And Circuits 4Th Ed. Bell, **Laboratory Manual (MultiSIM Emphasis) to Accompany Electronic Devices and Circuit Theory** Robert L. Boylestad, Louis Nashelsky, 2005-04 **Laboratory Manual for Introductory** Lab Manual for Electronic Devices, Global Edition **Electronics Experiments** L. K. Maheshwari, M. M. S. Anand, 1979 THOMAS L. FLOYD, 2018-06-19 This laboratory manual is carefully coordinated to the text Electronic Devices Tenth edition Global edition by Thomas L Floyd The seventeen experiments correspond to the chapters in the text except the first experiment references Chapters 1 and the first part of Chapter 2 All of the experiments are subdivided into two or three Parts With one exception Experiment 12 B the Parts for the all experiments are completely independent of each other The instructor can assign any or all Parts of these experiments and in any order This format provides flexibility depending on the schedule laboratory time available and course objectives In addition experiments 12 through 16 provide two options for experiments These five experiments are divided into two major sections identified as A or B The A experiments continue with the format of previous experiments they are constructed with discrete components on standard protoboards as used in most electronic teaching laboratories The A experiments can be assigned in programs where traditional devices are emphasized Each B experiment has a similar format to the corresponding A experiment but uses a programmable Analog Signal Processor ASP that is controlled by free Computer Aided Design CAD software from the Anadigm company www anadigm com These experiments support the Programmable Analog Design feature in the textbook The B experiments are also subdivided into independent Parts but Experiment 12 B Part 1 is a software tutorial and should be performed before any other B experiments This is an excellent way to introduce the ASP technology because no other hardware is required other than a computer running the downloaded software In addition to Experiment 12 B the first 13 steps of Experiment 15 B Part 2 are also tutorial in nature for the AnadigmFilter program This is an amazing active filter design tool that is easy to learn and is included with the AnadigmDesigner2 AD2 CAD software The ASP is part of a Programmable Analog Module PAM

circuit board from the Servenger company www servenger com that interfaces to a personal computer The PAM is controlled by the AD2 CAD software from the Anadigm company website Except for Experiment 12 B Part 1 it is assumed that the PAM is connected to the PC and AnadigmDesigner2 is running Experiment 16 B Part 3 also requires a spreadsheet program such as Microsoft Excel The PAM is described in detail in the Quick Start Guide Appendix B Instructors may choose to mix A and B experiments with no loss in continuity depending on course objectives and time We recommend that Experiment 12 B Part 1 be assigned if you want students to have an introduction to the ASP without requiring a hardware purchase A text feature is the Device Application DA at the end of most chapters All of the DAs have a related laboratory exercise using a similar circuit that is sometimes simplified to make laboratory time as efficient as possible The same text icon identifies the related DA exercise in the lab manual One issue is the trend of industry to smaller surface mount devices which are very difficult to work with and are not practical for most lab work For example almost all varactors are supplied as surface mount devices now In reviewing each experiment we have found components that can illustrate the device function with a traditional one The traditional through hole MV2109 varactor is listed as obsolete but will be available for the foreseeable future from Electronix Express www elexp com so it is called out in Experiment 3 All components are available from Electronix Express www elexp com as a kit of parts see list in Appendix A The format for each experiment has not changed from the last edition and is as follows Introduction A brief discussion about the experiment and comments about each of the independent Parts that follow Reading Reading assignment in the Floyd text related to the experiment Key Objectives A statement specific to each Part of the experiment of what the student should be able to do Components Needed A list components and small items required for each Part but not including the equipment found at a typical lab station Particular care has been exercised to select materials that are readily available and reusable keeping cost at a minimum Parts There are two or three independent parts to each experiment Needed tables graphs and figures are positioned close to the first referenced location to avoid confusion Step numbering starts fresh with each Part but figures and tables are numbered sequentially for the entire experiment to avoid multiple figures with the same number Conclusion At the end of each Part space is provided for a written conclusion Questions Each Part includes several questions that require the student to draw upon the laboratory work and check his or her understanding of the concepts Troubleshooting questions are frequently presented Multisim Simulation At the end of each A experiment except 1 one or more circuits are simulated in a Multisim computer simulation New Multisim troubleshooting problems have been added to this edition Multisim troubleshooting files are identified with the suffix f1 f2 etc in the file name standing for fault1 fault2 etc Other files with nf as the suffix include demonstrations or practice using instruments such as the Bode Plotter and the Spectrum Analyzer A special icon is shown with all figures that are related to the Multisim simulation Multisim files are found on the website www pearsonglobaledition com Floyd Microsoft PowerPoint slides are available at no cost to instructors for all experiments. The slides reinforce the experiments with troubleshooting

questions and a related problem and are available on the instructor's resource site Each laboratory station should contain a dual variable regulated power supply a function generator a multimeter and a dual channel oscilloscope A list of all required materials is given in Appendix A along with information on acquiring the PAM As mentioned components are also available as a kit from Electronix Express the kit number is 32DBEDFL10 ELECTRONICS LAB MANUAL (VOLUME 2) NAVAS, K. A., 2018-10-01 This book is evolved from the experience of the author who taught all lab courses in his three decades of teaching in various universities in India The objective of this lab manual is to provide information to undergraduate students to practice experiments in electronics laboratories This book covers 118 experiments for linear analog integrated circuits lab communication engineering lab power electronics lab microwave lab and optical communication lab The experiments described in this book enable the students to learn Various analog integrated circuits and their functions Analog and digital communication techniques Power electronics circuits and their functions Microwave equipment and components Optical communication devices This book is intended for the B Tech students of Electronics and Communication Engineering Electrical and Electronics Engineering Biomedical Electronics Instrumentation and Control Computer Science and Applied Electronics It is designed not only for engineering students but can also be used by BSc MSc Physics and Diploma students KEY FEATURES Contains aim components and equipment required theory circuit diagram pin outs of active devices design tables graphs alternate circuits and troubleshooting techniques for each experiment Includes viva voce and examination questions with their answers Provides exposure on various devices TARGET AUDIENCE B Tech Electronics and Communication Engineering Electrical and Electronics Engineering Biomedical Electronics Instrumentation and Control Computer Science and Applied Electronics BSc MSc Physics Diploma Engineering **Electronic Devices and Circuits** Khosrow Ghadiri, 2006-01-12 Electronic devices and circuit's laboratory manual for junior level college electronic design course The manual consist of ten experiments of multiple parts and six chapters of descriptions of the laboratory equipment such as dual display multimeter triple output DC power oscilloscope and function generator The manual also contains ten appendices of devices schematics and lab procedures This laboratory manual is designed to accompany one semester course or guarter class in electronic devices and circuit Each experiment in this manual should take one week to perform Normally students perform the experiments in groups of two Ideally a student more comfortable with the equipment used in this laboratory and especially the general purpose oscilloscope will be appointed group leader The function of the group leader is to supervise the activities of the group and become its spokesperson in its dealings with the laboratory instructor In those instances where the group leader has an extensive technical background he she should let the less experienced partner do most of the routine work limiting his her activities to checking and trouble shooting circuits as well as answering questions that may arise during the course of the experiment All parts of each experiment in this manual that students are to perform must be simulated with PSpice The simulations check the validity of the experimental measurements through theoretical

Laboratory Manual to Accompany Electronic Devices and Circuits and Electronic Devices and Circuits Conventional Flow Version Michael Hassul, Don Zimmerman, 1997-01-01 PSpice for Circuit Theory and Electronic Devices Paul Tobin, 2007 PSpice for Circuit Theory and Electronic Devices is one of a series of five PSpice books and introduces the latest Cadence Orcad PSpice version 10 5 by simulating a range of DC and AC exercises It is aimed primarily at those wishing to get up to speed with this version but will be of use to high school students undergraduate students and of course lecturers Circuit theorems are applied to a range of circuits and the calculations by hand after analysis are then compared to the simulated results The Laplace transform and the s plane are used to analyze CR and LR circuits where transient signals are involved Here the Probe output graphs demonstrate what a great learning tool PSpice is by providing the reader with a visual verification of any theoretical calculations Series and parallel tuned resonant circuits are investigated where the difficult concepts of dynamic impedance and selectivity are best understood by sweeping different circuit parameters through a range of values Obtaining semiconductor device characteristics as a laboratory exercise has fallen out of favour of late but nevertheless is still a useful exercise for understanding or modelling semiconductor devices Inverting and non inverting operational amplifiers characteristics such as gain bandwidth are investigated and we will see the dependency of bandwidth on the gain using the performance analysis facility Power amplifiers are examined where PSpice Probe demonstrates very nicely the problems of cross over distortion and other problems associated with power transistors We examine power supplies and the problems of regulation ground bounce and power factor correction Lastly we look at MOSFET device characteristics and show how these devices are used to form basic CMOS logic gates such as NAND and NOR gates

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