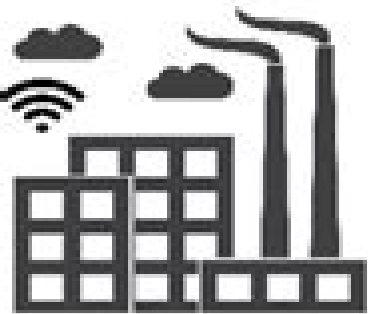




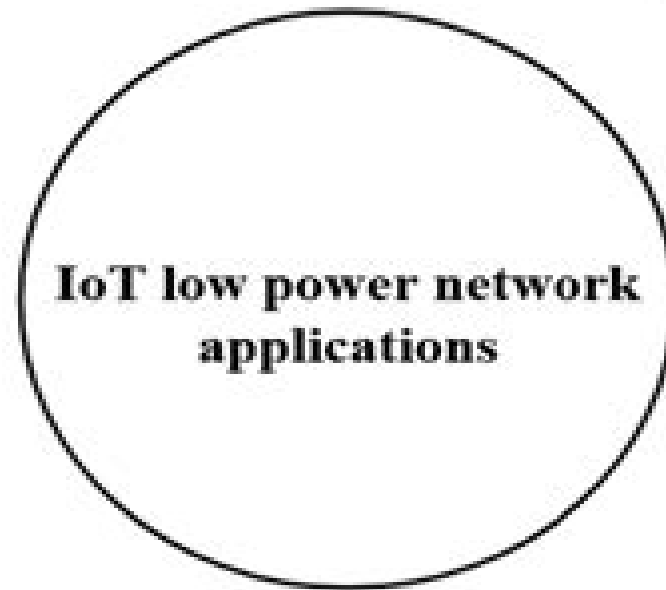
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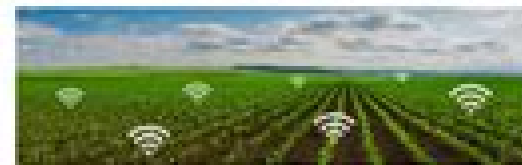
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Low Power Networks On Chip

Giovanni De Micheli, Luca Benini

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El-Moursy,Amr M. Gody,2012 Low leakage power switch is proposed to allow saving in power dissipation of the Network on Chip NoC Two power reduction techniques are exploited to design the proposed switch The proposed NoC switch employs power supply gating to reduce the power dissipation Adaptive virtual channel AVC is proposed as a novel technique to reduce power dissipation of the NoC switch AVC technique is proposed as an efficient technique to reduce the active area using hierarchical multiplexing tree Moreover power gating reduces the average leakage power consumption of proposed switch The proposed techniques reduce the leakage and dynamic power dissipation of the switch **Journal of VLSI Signal**

Processing Systems for Signal, Image, and Video Technology ,1997 *3D Networks-on-Chip Architecture*

Optimization for Low Power Design Opoku Agyeman Michael,2015-07-13 Three dimensional Networks on Chip 3D NoCs have attracted a growing interest to solve on chip communication demands of future multi core embedded systems However 3D NoCs have not been completely accepted into the mainstream due to issues such as the high cost and complexity of manufacturing 3D vertical wires larger memory area and power consumption of 3D NoC components than that of conventional 2D NoC This thesis aims at optimizing 3D NoCs by modeling and evaluating alternate NoC topologies routing algorithms and mapping techniques to achieve optimized area power and performance parameters latency and throughput Particularly novel 3D NoC router architectures and their possible combinations have been investigated with the aim of achieving lower area and power consumption of on chip communication components with a minimal performance trade off

This book investigates different heterogeneous 3D NoC architectures which combine 2D and 3D routers to improve area and energy efficiency of 3D NoCs with minimal performance degradation **Proceedings of the Technical Conference** ,1990
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