

Lab No. 01

INTRODUCTION TO DIGITAL LOGIC DESIGN LAB EQUIPMENT

Objective:

To Study the operation of different parts of Digital Analog Training System (ETS-7000)

Tools:

- 1- **Digital Analog Trainer (ETS-7000):**
The digital analog trainer is designed for beginners to enhance their concepts about digital and analog theory. It has a solderless breadboard with other parts like switches, LEDs, Seven Segment Display and other slots for wires.
- 2- **Breadboard:**
It is used for the prototyping and hardware simulation of a circuit. It is used to test and check a circuit before soldering the circuit components on a PCB or a Vero board. It makes the testing and analysis of a circuit, a reversible and easy to troubleshoot process unlike the analysis of a circuit on a Vero board.
- 3- **Jumper Wire:**
This wire is used with a breadboard to transmit electric signals. It is used as a conductor with high conductivity and low resistivity.
- 4- **Logic Indicator:**
This indicator is mounted on the trainer and is used to monitor the logic state of the circuit.
- 5- **Logic Switch:**
It is a diode arrangement present on the ETS-7000 trainer and is capable of directing an input signal.
- 6- **DC Power Supply:**
It is an electronic device used in order to supply DC energy to an electrical load.



Figure 1: ETS-7000 (Digital Analog Trainer)

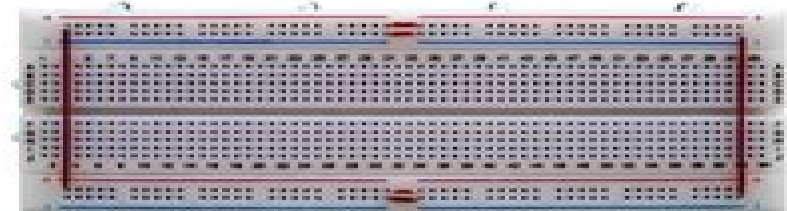


Figure 3: Jumper Wires



Logic Design Manual For

Gerald A. Maley



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Solutions manual Gideon Langholz, Abraham Kandel, Joe L. Mott, 1988 **Digital Logic Design** John F. Passafiume, Michael Douglas, 1985 **Introduction to Digital Logic Design** John Patrick Hayes, 1993-01-01

Fundamentals of Digital Logic and Microcomputer Design M. Rafiquzzaman, 2005-06-06 Fundamentals of Digital Logic and Microcomputer Design has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers In this Fifth Edition the author focuses on computer design at three levels the device level the logic level and the system level Basic topics are covered such as number systems and Boolean algebra combinational and sequential logic design as well as more advanced subjects such as assembly language programming and microprocessor based system design Numerous examples are provided throughout the text Coverage includes Digital circuits at the gate and flip flop levels Analysis and design of combinational and sequential circuits Microcomputer organization architecture and programming concepts Design of computer instruction sets CPU memory and I O System design features associated with popular microprocessors from Intel and Motorola Future plans in microprocessor development An instructor s manual available upon request Additionally the accompanying CD ROM contains step by step procedures for installing and using Altera Quartus II software MASM 6 11 8086 and 68asmsim 68000 provides valuable simulation results via screen shots Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems **Solutions Manual for Digital Logic and State Machine Design** David J. Comer, 1984 *Introduction to Logic Design, Second Edition* Sajjan G. Shiva, 1998-01-20 The second edition of this text provides an introduction to the analysis and design of digital circuits at a logic instead of electronics level It covers a range of topics from number system theory to asynchronous logic design A solution manual is available to instructors only Requests must be made on official school stationery **The Logic Design of Transistor Digital Computers** Gerald A. Maley, John Earle, 1963 **Fundamentals of Digital Logic with Verilog Design** Stephen Brown, Professor, Zvonko Vranesic, 2013-02-12 Fundamentals of Digital Logic With Verilog Design is intended for an introductory course in digital logic design The main goals are 1 to teach students the fundamental concepts in classical manual digital design and 2 illustrate clearly the way in which digital circuits are designed today using CAD tools Use of CAD software is well integrated into the book Some excellent CAD tools are available free of charge For example the Altera Corporation has its Quartus II CAD software used for implementing designs in programmable logic devices such as FPGAs The Web Edition of the Quartus II software can be downloaded from Altera s website and used free of charge without the need to obtain a license Previous editions of this book a set of tutorials for using Quartus II software was provided in the appendices These tutorials can now be found on the Author s website Another set of useful tutorials about Quartus II can be found on Altera s University Program website which is located at www.altera.com/education/univ **Manual of Logic**

Circuits Gerald A. Maley,1970 **An Introductory Guide to EC Competition Law and Practice** Valentine Korah,1994

Computerized Logic Design of Digital Circuits ,1973 This manual presents a computer program that performs all the work required for the logic design of digital counters or sequential circuits and the simplification of Boolean logic expressions The program provides both the experienced and inexperienced logic designer with a comprehensive logic design capability The manual contains Boolean simplification and sequential design theory detailed instructions for use of the program a large number of illustrative design examples and complete program documentation **Digital Logic Design,**

Lab Manual, Volume-I Asadullah Khan,2017-03-24 *Computer Architecture and Logic Design* Thomas C. Bartee,1991

Machine Design; a Manual of Practical Instruction in the Art of Creating Machinery for Specific Purposes American School (Lansing, Ill.),1907 Logic Designer's Manual John D. Lenk,1977 Dewdney s careful recording gives a baseline for preserving these important messages some of which are from deep human past These paintings some of which are very artistic are in danger of disappearing due to human activity and natural causes which makes the preservation so important Dewdney himself made major contributions to the field of art therapy and approached the rock paintings with a respect which was not always common in his day reader s review Logical Design for Digital Instrumentation Systems University

of Michigan. Engineering Summer Conferences,1978 **Machine Design** American School of Correspondence,Charles Lewis Griffin,1907 Fundamentals of Logic Design Charles H. Roth,1992 **Engineering Digital Design** Richard F.

Tinder,2000-01-18 Engineering Digital Design Second Edition provides the most extensive coverage of any available textbook in digital logic and design The new REVISED Second Edition published in September of 2002 provides 5 productivity tools free on the accompanying CD ROM This software is also included on the Instructor s Manual CD ROM and complete instructions accompany each software program In the REVISED Second Edition modern notation combines with state of the art treatment of the most important subjects in digital design to provide the student with the background needed to enter industry or graduate study at a competitive level Combinatorial logic design and synchronous and asynchronous sequential machine design methods are given equal weight and new ideas and design approaches are explored The productivity tools provided on the accompanying CD are outlined below 1 EXL Sim2002 logic simulator EXL Sim2002 is a full featured interactive schematic capture and simulation program that is ideally suited for use with the text at either the entry or advanced level of logic design Its many features include drag and drop capability rubber banding mixed logic and positive logic simulations macro generation individual and global or randomized delay assignments connection features that eliminate the need for wire connections schematic page sizing and zooming waveform zooming and scrolling a variety of printout capabilities and a host of other useful features 2 BOOZER logic minimizer BOOZER is a software minimization tool that is recommended for use with the text It accepts entered variable EV or canonical 1 s and 0 s data from K maps or truth tables with or without don t cares and returns an optimal or near optimal single or multi output solution It can handle up to 12

functions Boolean functions and as many inputs when used on modern computers 3 ESPRESSO II logic minimizer ESPRESSO II is another software minimization tool widely used in schools and industry It supports advanced heuristic algorithms for minimization of two level multi output Boolean functions but does not accept entered variables It is also readily available from the University of California Berkeley 1986 VLSI Tools Distribution 4 ADAM design software ADAM for Automated Design of Asynchronous Machines is a very powerful productivity tool that permits the automated design of very complex asynchronous state machines all free of timing defects The input files are state tables for the desired state machines The output files are given in the Berkeley format appropriate for directly programming PLAs ADAM also allows the designer to design synchronous state machines timing defect free The options include the lumped path delay LPD model or NESTED CELL model for asynchronous FSM designs and the use of D FLIP FLOPs for synchronous FSM designs The background for the use of ADAM is covered in Chapters 11 14 and 16 of the REVISED 2nd Edition 5 A OPS design software A OPS for Asynchronous One hot Programmable Sequencers is another very powerful productivity tool that permits the design of asynchronous and synchronous state machines by using a programmable sequencer kernel This software generates a PLA or PAL output file in Berkeley format or the VHDL code for the automated timing defect free designs of the following a Any 1 Hot programmable sequencer up to 10 states b The 1 Hot design of multiple asynchronous or synchronous state machines driven by either PLDs or RAM The input file is that of a state table for the desired state machine This software can be used to design systems with the capability of instantly switching between several radically different controllers on a time shared basis The background for the use of A OPS is covered in Chapters 13 14 and 16 of the REVISED 2nd Edition **The Logical Design of Parallel Operating Systems** Wolfgang Schröder-Preikschat, 1994

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