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1800

Your Roll No.

B.Sc. (Hons.) Computer Sc. / V Sem. Paper – 503 : MICROPROCESSORS (Admissions of 2001 and onwards)



Maximum Marks: 75

(Write your Roll No. on the top immediately on receipt of this question paper.)

Attempt all questions.

Parts of questions should be attempted together.

- (a) Differentiate between Trap (T) and Interrupt flag
 (l) of EFLAG register.
 - (b) Find the memory address of the next instruction executed by the microprocessor, when operated in the real mode, for the following CS: IP combinations:
 - (i) CS = 2300 H and IP = 1A00H
 - (ii) CS = 3456 H and IP = AB CDH
 - (c) What value is placed in the page table to redirect linear address 200000000 H - 300000000 H ? 2
 - (d) What are microprocessor control registers in memory paging?

[P.T.O.

Microprocessor And Its Applications Question Paper

Marcel A. Müller

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