

The background of the cover is a high-magnification, false-color micrograph of a silicon microchip die. The die is rectangular and densely packed with intricate circuit patterns. Various functional blocks are highlighted in different colors: large green rectangular blocks, smaller red blocks, and purple/blue areas. A network of fine, light-colored lines represents the interconnects across the surface. The overall image has a technical, scientific feel.

FOURTH EDITION

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# Manual For Cmos Vlsi

**David Kirk**



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