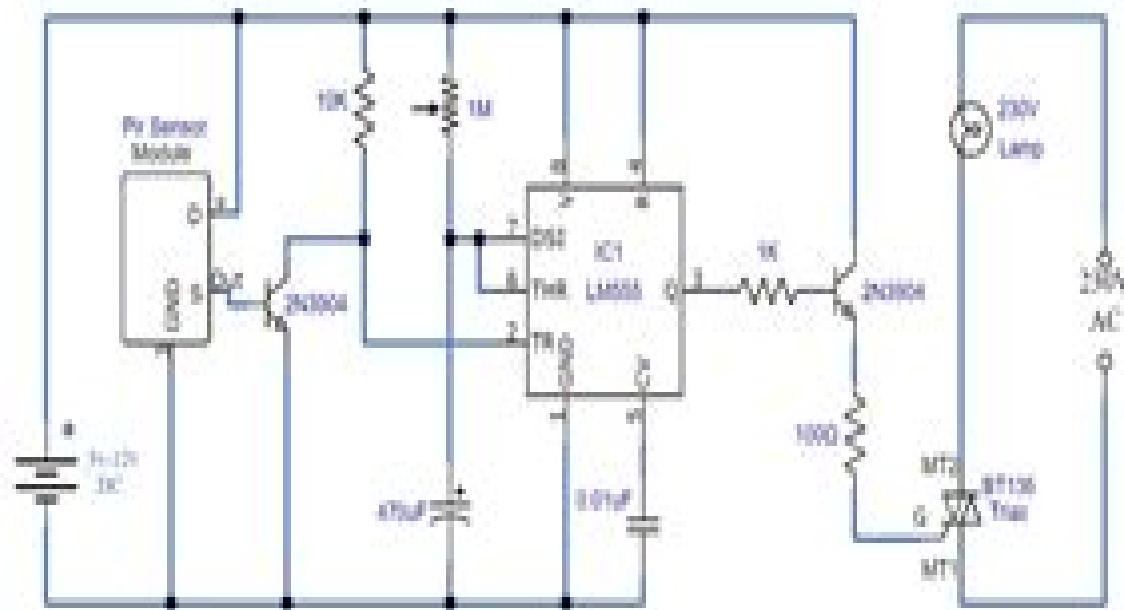


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Steve Ciarcia

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challenges Technical problems have known solutions that most designers clearly understand However this means they will solve problems using existing organizational practices An adaptive challenge means the organization will face problems that individually have many possible solutions To find the correct set of solutions the organization must experiment and adapt over time Many design organizations ignore the fundamental differences between technical problems and adaptive challenges As a result engineering and IT planners mistakenly believe that they only need to hire specialists to solve technical problems They expect these specialists to use the latest technologies and or adopt some agile development process These technology focused designs or faith based processes produce applications that have many undesirable anomalies idiosyncrasies and outliers The information contained in this book enables strategists to stop adapting to challenges and start solving problems The information defines and describes how low level design fundamentals affect manufacturing processes and upper level system designs It specifically identifies the many technical problems designers will face variable methods for solving them and expected outcomes This information enables an organization to adopt the best practices before starting a design This sets up a knowledge based development process where designers understand technical problems adopt the correct set of fundamentals and make the necessary improvements to machines and system designs

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systems design context Rather than focus on aspects of digital design that have little relevance in a realistic design context this book concentrates on modern and evolving knowledge and design skills Hardware description language HDL based design and verification is emphasized Verilog examples are used extensively throughout By treating digital logic as part of embedded systems design this book provides an understanding of the hardware needed in the analysis and design of systems comprising both hardware and software components Includes a Web site with links to vendor tools labs and tutorials Presents digital logic design as an activity in a larger systems design context Features extensive use of Verilog examples to demonstrate HDL hardware description language usage at the abstract behavioural level and register transfer level as well as for low level verification and verification environments Includes worked examples throughout to enhance the reader s understanding and retention of the material Companion Web site includes links to tools for FPGA design from Synplicity Mentor Graphics and Xilinx Verilog source code for all the examples in the book lecture slides laboratory projects and solutions to exercises

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