

Item	Alarm	Detection Method/Condition	Detecting Location in Logic Card
E0	Right Motor, Overheating	Thermal switch signal input for right motor [150°C (302°F)]	CN1-26
E1	Left Motor, Overheating	Thermal switch signal input for left motor [150°C (302°F)]	CN1-28
E2	Pump Motor, Overheating	Thermal switch signal input for pump motor [150°C (302°F)]	CN1-30
E3	Controller, Overheating	Thermal sensor signal input for controller [80°C (176°F) thermistor]	CN2-43
E4	Right Motor, Worn Brushes	Brush wear signal input for right motor	CN1-27
E5	Left Motor, Worn Brushes	Brush wear signal input for left motor	CN1-29
E6	Pump Motor, Worn Brushes	Brush wear signal input for pump motor	CN1-31
	Brake Oil, Low Level	Level sensor signal input for brake oil	(CN1-32)
10	Right Motor, Faulty Armature Circuit	Armature error under various conditions	Combination from CN2-34, 39, 41, 45
11	Right Motor, Transistor Wire Breakage	Armature error under ARM output ON	Combination from CN2-34, 39, 41, 45
12	Right Motor, Shorted Transistor	Armature error under ARM output OFF	Combination from CN2-34, 39, 41, 45
13	Right Motor, Faulty Transistor Drive	Abnormal over-current for right IGBT of drive board	CN2-30 is less than 1 V
14	Right Motor, Faulty Current Sensor	Right current sensor error under ARM output OFF	Combination from CN2-34, 39, 41, 45
15	Right Motor, Armature Over-current	Right motor armature current is 350 A or more	CN2-45 is 2.45 V or more
16	Right Motor, Stall Timer	The time set with setup option elapsed	
17	Right Motor, Faulty Field Circuit	Error in line voltage or field current	Combination from CN2-15, 34
18	Right Motor, Faulty Field Drive	H bridge board detected abnormal over current for right FET	CN2-13 is less than 1 V
19	Right Motor, Field Over-current	Right motor field over-current is 45 A or more (continues 5 msec)	CN2-15 is 4.5 V or more
20	Left Motor, Faulty Armature Circuit	Armature error under various conditions	Combination from CN2-34, 39, 42, 47
21	Left Motor, Transistor Wire Breakage	Armature error under ARM output ON	Combination from CN2-34, 39, 42, 47
22	Left Motor, Shorted Transistor	Armature error under ARM output OFF	Combination from CN2-34, 39, 42, 47
23	Left Motor, Faulty Transistor Drive	Abnormal detection signal from drive board	CN2-31 is less than 1 V
24	Left Motor, Faulty Current Sensor	Left current sensor error under ARM output OFF	Combination from CN2-34, 39, 42, 47
25	Left Motor, Armature Over-current	Left motor armature current is 350 A or more	CN2-47 is 2.45 V or more
26	Left Motor, Stall Timer	The time set with setup option elapsed	
27	Left Motor, Faulty Field Circuit	Error in line voltage or field current	Combination from CN2-17, 34
28	Left Motor, Faulty Field Drive	H bridge board detected abnormal over-current for left FET	CN2-14 is less than 1 V
29	Left Motor, Field Over-current	Left motor field over-current is 45 A or more (continues 5 msec)	CN2-17 is 4.5 V or more
30	Pump Motor, Faulty Circuit	Checks voltage level and current level for main circuit	Combination from CN2-34, 39, 40, 49
31	Pump Motor, Transistor Wire Breakage	Checks voltage level and current level for main circuit	Combination from CN2-34, 39, 40, 49
32	Pump Motor, Shorted Transistor	Checks voltage level and current level for main circuit	Combination from CN2-34, 39, 40, 49
33	Pump Motor, Faulty Transistor Drive	Abnormal detection signal from drive board	CN2-32 is less than 1 V

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Information Theory, Inference and Learning Algorithms David J. C. MacKay, 2003-09-25 Information theory and inference taught together in this exciting textbook lie at the heart of many important areas of modern technology communication signal processing data mining machine learning pattern recognition computational neuroscience bioinformatics and cryptography The book introduces theory in tandem with applications Information theory is taught alongside practical communication systems such as arithmetic coding for data compression and sparse graph codes for error correction Inference techniques including message passing algorithms Monte Carlo methods and variational approximations are developed alongside applications to clustering convolutional codes independent component analysis and neural networks Uniquely the book covers state of the art error correcting codes including low density parity check codes turbo codes and digital fountain codes the twenty first century standards for satellite communications disk drives and data broadcast Richly illustrated filled with worked examples and over 400 exercises some with detailed solutions the book is ideal for self learning and for undergraduate or graduate courses It also provides an unparalleled entry point for professionals in areas as diverse as computational biology financial engineering and machine learning

The Transactions of the Institute of Electronics and Communication Engineers of Japan Denshi Tsūshin Gakkai, 1986

Algebraic and Stochastic Coding Theory Dave K. Kythe, Prem K. Kythe, 2017-07-28 Using a simple yet rigorous approach Algebraic and Stochastic Coding Theory makes the subject of coding theory easy to understand for readers with a thorough knowledge of digital arithmetic Boolean and modern algebra and probability theory It explains the underlying principles of coding theory and offers a clear detailed description of each code More advanced readers will appreciate its coverage of recent developments in coding theory and stochastic processes After a brief review of coding history and Boolean algebra the book introduces linear codes including Hamming and Golay codes It then examines codes based on the Galois field theory as well as their application in BCH and especially the Reed Solomon codes that have been used for error correction of data transmissions in space missions The major outlook in coding theory seems to be geared toward stochastic processes and this book takes a bold step in this direction As research focuses on error correction and recovery of erasures the book discusses belief propagation and distributions It examines the low density parity check and erasure codes that have opened up new approaches to improve wide area network data transmission It also describes modern codes such as the Luby transform and Raptor codes that are enabling new directions in high speed transmission of very large data to multiple users This robust self contained text fully explains coding problems illustrating them with more than 200 examples Combining theory and computational techniques it will appeal not only to students but also to industry professionals researchers and academics in areas such as coding theory and signal and image processing

High Spectral Density Optical Communication Technologies Masataka Nakazawa, Kazuro Kikuchi, Tetsuya Miyazaki, 2010-08-12 The growth of Internet traffic in recent years surpassed the prediction of one decade ago Data stream in

individual countries already reached terabit/s level. To cope with the petabit class demands of traffic in coming years the communication engineers are required to go beyond the incremental improvement of today's technology. A most promising breakthrough would be the introduction of modulation formats enabling higher spectral efficiency than that of binary on-off keying scheme, virtually the global standard of fiber optic communication systems. In wireless communication systems techniques of high spectral density modulation have been well developed but the required techniques in optical frequency domain are much more complicated because of the heavier fluctuation levels. Therefore the past trials of coherent optical modulation detection schemes were not successful. However the addition of high speed digital signal processing technology is the fundamental difference between now and two decades ago when trials of optical coherent communication systems were investigated very seriously. This approach of digital coherent technology has attracted keen interest among communication specialists as indicated by the rapid increase in the pioneering presentations at the post deadline sessions of major international conferences. For example 32 terabit/s transmission in a fiber experiment based on this technology was reported in post deadline session of Optical Fiber Communication Conference OFC 2009. The advancement of the digital coherent technologies will inevitably affect the network architecture in terms of the network resource management for the new generation photonic networks rather than will simply provide with huge transmission capacity.

The Transactions of the Institute of Electronics, Information and Communication Engineers, 1990 Official Gazette of the United States Patent and Trademark Office, 1999 Science Abstracts, 1995 1986 IEEE International Symposium on Information Theory (ISIT), 1986 **IEEE International Symposium on Information Theory**, 1988 Government Reports Announcements & Index, 1995 2004 IEEE International Symposium on Information Theory : Proceedings : Chicago Downtown Marriott, Chicago, Illinois, USA, June 27-July 2, 2004, 2004 Abstracts of Science and Technology in Japan, 1992 Japanese Technical Abstracts, 1987 **Electrical & Electronics Abstracts**, 1997 **1984 Digests of Intermag '84**, 1984 **Journal of the Audio Engineering Society** Audio Engineering Society, 1984 Conference Record, 1978 National Telecommunications Conference, Birmingham, Alabama, Dec. 3-6, 1978, 1978 Mathematical Foundations of Computer Science, 2004 **IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences**, 1992 *Video, Audio and Data Recording*, 1988

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