


## SMD MOSFET Transistors (P-Channel)

Part No.	Drain-Source Breakdown Voltage	Min. On-State DS Current	Typ. Static DS Resistance	Part No.	Drain-Source Breakdown Voltage	Min. On-State DS Current	Typ. Static DS Resistance	Package	Outline (Typ. in inches)
	$BV_{DS}(V)$	$I_{DS(on)}(A)$	$R_{DS(on)}(\Omega)$		$BV_{DS}(V)$	$I_{DS(on)}(A)$	$R_{DS(on)}(\Omega)$	Reel	
IRPFR010	-50	-5.3	0.50	IRPFR0220	-200	-3.6	1.5	2000	<b>D-PACK</b> 

# Mosfet Replacement Guide

**Rachel S Tattersall**



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,1987      **Popular Electronics** ,1977      **Radio-electronics** ,1987      **Springer Handbook of Semiconductor Devices**  
Massimo Rudan, Rossella Brunetti, Susanna Reggiani, 2022-11-10 This Springer Handbook comprehensively covers the topic of  
semiconductor devices embracing all aspects from theoretical background to fabrication modeling and applications Nearly  
100 leading scientists from industry and academia were selected to write the handbook s chapters which were conceived for  
professionals and practitioners material scientists physicists and electrical engineers working at universities industrial R D  
and manufacturers Starting from the description of the relevant technological aspects and fabrication steps the handbook  
proceeds with a section fully devoted to the main conventional semiconductor devices like e g bipolar transistors and MOS  
capacitors and transistors used in the production of the standard integrated circuits and the corresponding physical models

In the subsequent chapters the scaling issues of the semiconductor device technology are addressed followed by the description of novel concept based semiconductor devices The last section illustrates the numerical simulation methods ranging from the fabrication processes to the device performances Each chapter is self contained and refers to related topics treated in other chapters when necessary so that the reader interested in a specific subject can easily identify a personal reading path through the vast contents of the handbook

### **MOS Devices for Low-Voltage and Low-Energy Applications**

Yasuhisa Omura, Abhijit Mallik, Naoto Matsuo, 2017-02-28 Helps readers understand the physics behind MOS devices for low voltage and low energy applications Based on timely published and unpublished work written by expert authors Discusses various promising MOS devices applicable to low energy environmental and biomedical uses Describes the physical effects quantum tunneling of MOS devices Demonstrates the performance of devices helping readers to choose right devices applicable to an industrial or consumer environment Addresses some Ge based devices and other compound material based devices for high frequency applications and future development of high performance devices Seemingly innocuous everyday devices such as smartphones tablets and services such as on line gaming or internet keyword searches consume vast amounts of energy Even when in standby mode all these devices consume energy The upcoming Internet of Things IoT is expected to deploy 60 billion electronic devices spread out in our homes cars and cities Britain is already consuming up to 16 per cent of all its power through internet use and this rate is doubling every four years According to The UK's Daily Mail May 2015 if usage rates continue all of Britain's power supply could be consumed by internet use in just 20 years In 2013 U S data centers consumed an estimated 91 billion kilowatt hours of electricity corresponding to the power generated by seventeen 1000 megawatt nuclear power plants Data center electricity consumption is projected to increase to roughly 140 billion kilowatt hours annually by 2020 the equivalent annual output of 50 nuclear power plants Natural Resources Defense Council USA Feb 2015 All these examples stress the urgent need for developing electronic devices that consume as little energy as possible The book MOS Devices for Low Voltage and Low Energy Applications explores the different transistor options that can be utilized to achieve that goal It describes in detail the physics and performance of transistors that can be operated at low voltage and consume little power such as subthreshold operation in bulk transistors fully depleted SOI devices tunnel FETs multigate and gate all around MOSFETs Examples of low energy circuits making use of these devices are given as well The book MOS Devices for Low Voltage and Low Energy Applications is a good reference for graduate students researchers semiconductor and electrical engineers who will design the electronic systems of tomorrow Dr Jean Pierre Colinge Taiwan Semiconductor Manufacturing Company TSMC The authors present a creative way to show how different MOS devices can be used for low voltage and low power applications They start with Bulk MOSFET following with SOI MOSFET FinFET gate all around MOSFET Tunnel FET and others It is presented the physics behind the devices models simulations experimental results and applications This book is interesting for researchers graduate and undergraduate

students The low energy field is an important topic for integrated circuits in the future and none can stay out of this Prof Joao A Martino University of Sao Paulo Brazil *IEDM Technical Digest* ,1985 *CQ* ,1994 *Peterson's Guide to Graduate Programs in Engineering and Applied Sciences* ,1991 *Characterization and Metrology for ULSI Technology: 2003* David G. Seiler,2003-10-08 The worldwide semiconductor community faces increasingly difficult challenges as it moves into the manufacturing of chips with feature sizes approaching 100 nm and beyond The magnitude of these challenges demands special attention from the metrology and analytical measurements community New paradigms must be found Adequate research and development for new metrology concepts are urgently needed Topics include integrated circuit history challenges and overviews front end lithography interconnect and back end and critical analytical techniques Characterization and metrology are key enablers for developing new semiconductor technology and in improving manufacturing This book summarizes major issues and gives critical reviews of important measurement techniques that are crucial to continue the advances in semiconductor technology It covers major aspects of process technology and most characterization techniques for silicon research including development manufacturing and diagnostics The editors believe that this book of collected papers provides a concise and effective portrayal of industry characterization needs and the way they are being addressed by industry academia and government to continue the dramatic progress in semiconductor technology Hopefully it will also provide a basis for stimulating advances in metrology and new ideas for research and development *Electronic Products Magazine* ,1995

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