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coefficients c_i 0 1 for $0 \leq i \leq n-1$ the feedback function which is addition mod 2 the xor function computing the new state value
 s_n using the coefficients and the state values as s_n

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ing them with the output of an identical lfsr

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two or more of its previous states taps an lfsr of length m consists of m stages numbered $0 \leq i \leq m-1$ each capable of storing one
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registers with a small number of xor gates used for random number generation counters error checking and correction

advantages very little hardware high speed operation example 4 bit lfsr $q_4 \ q \ d \ q_3 \ q \ d \ q_2 \ q \ d \ q_1 \ q$

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take an lfsr of the same size as the period it is therefore natural to use the length

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